





IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Majid et al.	)	Examiner: Parekh, N.	
Application No.: 09/440,595	)	Art Unit: 2811	
Filed: 11/15/1999	)		REC DEC
For: MULTIPLE SEMICONDUCTOR CHIP (MULTI-CHIP) MODULE FOR USE IN POWER APPLICATIONS	) )		ENE 27 Day of
Box Non-Fee Amendment Commissioner for Patents Washington D.C. 20231		,	2002 2002 2002

## REQUEST FOR RECONSIDERATION

Sir:

In response to the Office Action of September 25, 2002, Applicants respectively request that the above-identified application be reconsidered in view of the remarks that follow, that each of the presently pending claims be allowed, and that the application be passed to issue.

## **REMARKS**

Claims 1-7 are pending.

Claims 1-7 are rejected under 35 U.S.C. §103(a) as being unpatentable over Uchida et al. (US 6,060,748), "Uchida," in view of Oka et al. (US 6,140,690), Takagi et al. (US 6,130,458), and Lauffer et al. (EP 0 471 938 A1). This rejection is defective because the cited references, taken alone or in any combination, fail to teach or suggest each and every feature of the present

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